

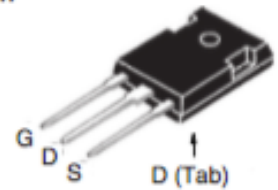
**X2-Class HiPerFET™  
Power MOSFET**
**IXFH34N65X2**

$$\begin{aligned} V_{DSS} &= 650V \\ I_{D25} &= 34A \\ R_{DS(on)} &\leq 105m\Omega \end{aligned}$$

N-Channel Enhancement Mode  
Avalanche Rated  
Fast Intrinsic Diode



TO-247



G = Gate      D = Drain  
S = Source    Tab = Drain

Symbol	Test Conditions	Maximum Ratings	
$V_{DSS}$	$T_J = 25^\circ\text{C to } 150^\circ\text{C}$	650	V
$V_{DSM}$	$T_J = 25^\circ\text{C to } 150^\circ\text{C}, R_{GS} = 1M\Omega$	650	V
$V_{GS}$	Continuous	$\pm 30$	V
$V_{GSM}$	Transient	$\pm 40$	V
$I_{D25}$	$T_C = 25^\circ\text{C}$	34	A
$I_{DM}$	$T_C = 25^\circ\text{C}, \text{ Pulse Width Limited by } T_{JM}$	68	A
$I_A$	$T_C = 25^\circ\text{C}$	5	A
$E_{AS}$	$T_C = 25^\circ\text{C}$	1.25	J
$dv/dt$	$I_B \leq I_{DM}, V_{DD} \leq V_{DSS}, T_J \leq 150^\circ\text{C}$	50	V/ns
$P_D$	$T_C = 25^\circ\text{C}$	540	W
$T_J$		-55 ... +150	$^\circ\text{C}$
$T_{JM}$		150	$^\circ\text{C}$
$T_{stg}$		-55 ... +150	$^\circ\text{C}$
$T_L$	Maximum Lead Temperature for Soldering	300	$^\circ\text{C}$
$T_{SOLD}$	1.6 mm (0.062in.) from Case for 10s	260	$^\circ\text{C}$
$M_D$	Mounting Torque	1.13 / 10	Nm/lb.in
Weight		6	g

**Features**

- International Standard Package
- Low  $R_{DS(on)}$  and  $Q_D$
- Avalanche Rated
- Low Package Inductance

**Advantages**

- High Power Density
- Easy to Mount
- Space Savings

**Applications**

- Switch-Mode and Resonant-Mode Power Supplies
- DC-DC Converters
- PFC Circuits
- AC and DC Motor Drives
- Robotics and Servo Controls

Symbol	Test Conditions ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
$BV_{DSS}$	$V_{GS} = 0V, I_D = 1mA$	650		V
$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 2.5mA$	2.7		5.5 V
$I_{GSS}$	$V_{GS} = \pm 30V, V_{DS} = 0V$			$\pm 100$ nA
$I_{DSS}$	$V_{DS} = V_{DSS}, V_{GS} = 0V$ $T_J = 125^\circ\text{C}$			25 $\mu\text{A}$ 2.5 mA
$R_{DS(on)}$	$V_{GS} = 10V, I_D = 0.5 \cdot I_{D25}$ , Note 1			105 m $\Omega$

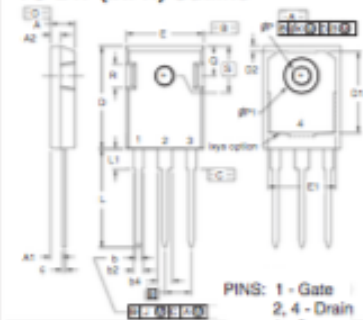
Symbol	Test Conditions ( $T_j = 25^\circ\text{C}$ , Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max
$g_{fs}$	$V_{DS} = 10\text{V}$ , $I_D = 0.5 \cdot I_{D25}$ , Note 1	14	24	S
$R_{GS}$	Gate Input Resistance		0.8	$\Omega$
$C_{iss}$	$V_{GS} = 0\text{V}$ , $V_{DS} = 25\text{V}$ , $f = 1\text{MHz}$		3330	pF
$C_{oss}$			2190	pF
$C_{rss}$			2	pF
$t_{d(on)}$	<b>Resistive Switching Times</b> $V_{GS} = 10\text{V}$ , $V_{DS} = 0.5 \cdot V_{DSS}$ , $I_D = 0.5 \cdot I_{D25}$ $R_G = 10\Omega$ (External)		46	ns
$t_r$			45	ns
$t_{d(off)}$			47	ns
$t_f$			16	ns
$Q_{g(on)}$	$V_{GS} = 10\text{V}$ , $V_{DS} = 0.5 \cdot V_{DSS}$ , $I_D = 0.5 \cdot I_{D25}$		56	nC
$Q_{gs}$			28	nC
$Q_{gd}$			14	nC
$R_{thJC}$			0.23	$^\circ\text{C/W}$
$R_{thCS}$		0.21		$^\circ\text{C/W}$

### Source-Drain Diode

Symbol	Test Conditions ( $T_j = 25^\circ\text{C}$ , Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max
$I_S$	$V_{GS} = 0\text{V}$			34 A
$I_{SM}$	Repetitive, pulse Width Limited by $T_{JM}$			136 A
$V_{SD}$	$I_F = I_S$ , $V_{GS} = 0\text{V}$ , Note 1			1.4 V
$t_{rr}$	$I_F = 17\text{A}$ , $-di/dt = 100\text{A}/\mu\text{s}$ $V_R = 100\text{V}$		160	ns
$Q_{RM}$			1.1	$\mu\text{C}$
$I_{RM}$			13.0	A

Note 1. Pulse test,  $t \leq 300\mu\text{s}$ , duty cycle,  $d \leq 2\%$ .

### TO-247 (IXFH) Outline



SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.150	.200	4.00	5.20
A1	.250	.300	6.35	7.62
A2	.075	.095	1.91	2.41
B	.045	.055	1.14	1.40
B2	.075	.097	1.91	2.46
D4	.115	.125	2.92	3.18
C	.024	.031	0.61	0.79
D	.810	.840	20.57	21.34
D1	.650	.670	16.51	17.27
D2	.630	.650	16.01	16.77
E	.600	.620	15.25	15.73
F1	.540	.560	13.84	14.20
e	.210 35C		5.45 35C	
J	---	.050	---	1.27
K	---	.020	---	0.51
L	.700	.810	17.81	20.57
L1	.150	.170	3.81	4.30
ØP	.140	.144	3.55	3.65
ØP1	.275	.290	6.99	7.37
O	.200	.214	5.08	5.43
S	.170	.190	4.30	4.83
s	.242 35C		6.15 35C	

### ADVANCE TECHNICAL INFORMATION

The product presented herein is under development. The Technical Specifications offered are derived from a subjective evaluation of the design, based upon prior knowledge and experience, and constitute a "considered reflection" of the anticipated result. IXYS reserves the right to change limits, test conditions, and dimensions without notice.

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IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents: 4,835,582 4,931,844 5,049,961 5,237,481 6,162,665 6,404,065B1 6,683,344 6,727,585 7,005,734B2 7,157,336B2  
4,860,072 5,017,508 5,063,307 5,381,025 6,259,123B1 6,534,343 6,710,405B2 6,759,692 7,063,975B2  
4,881,106 5,034,796 5,187,117 5,486,715 6,306,728B1 6,583,505 6,710,463 6,771,478B2 7,071,537