

**X2-Class HiPerFET™
Power MOSFET**
IXFH80N65X2

$$V_{DSS} = 650V$$

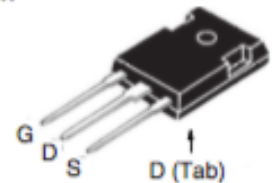
$$I_{D25} = 80A$$

$$R_{DS(on)} \leq 40m\Omega$$

N-Channel Enhancement Mode
Avalanche Rated
Fast Intrinsic Diode



TO-247



G = Gate D = Drain
S = Source Tab = Drain

Symbol	Test Conditions	Maximum Ratings	
V_{DSS}	$T_J = 25^\circ\text{C}$ to 150°C	650	V
V_{DSR}	$T_J = 25^\circ\text{C}$ to 150°C , $R_{GS} = 1M\Omega$	650	V
V_{GSS}	Continuous	± 30	V
V_{GSM}	Transient	± 40	V
I_{D25}	$T_C = 25^\circ\text{C}$	80	A
I_{DM}	$T_C = 25^\circ\text{C}$, Pulse Width Limited by T_{JM}	160	A
I_A	$T_C = 25^\circ\text{C}$	10	A
E_{AS}	$T_C = 25^\circ\text{C}$	700	mJ
dv/dt	$I_B \leq I_{DM}$, $V_{DD} \leq V_{DSS}$, $T_J \leq 150^\circ\text{C}$	50	V/ns
P_D	$T_C = 25^\circ\text{C}$	890	W
T_J		-55 ... +150	$^\circ\text{C}$
T_{JM}		150	$^\circ\text{C}$
T_{stg}		-55 ... +150	$^\circ\text{C}$
T_L	Maximum Lead Temperature for Soldering	300	$^\circ\text{C}$
T_{SOLD}	1.6 mm (0.062in.) from Case for 10s	260	$^\circ\text{C}$
M_d	Mounting Torque	1.13 / 10	Nm/lb.in
Weight		6	g

Features

- International Standard Package
- Low $R_{DS(on)}$ and Q_D
- Avalanche Rated
- Low Package Inductance

Advantages

- High Power Density
- Easy to Mount
- Space Savings

Applications

- Switch-Mode and Resonant-Mode Power Supplies
- DC-DC Converters
- PFC Circuits
- AC and DC Motor Drives
- Robotics and Servo Controls

Symbol	Test Conditions ($T_J = 25^\circ\text{C}$, Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
BV_{DSS}	$V_{GS} = 0V$, $I_D = 1mA$	650		V
$V_{GS(th)}$	$V_{DS} = V_{DSS}$, $I_D = 4mA$	2.7		5.5 V
I_{GSS}	$V_{GS} = \pm 30V$, $V_{DS} = 0V$			± 100 nA
I_{DSS}	$V_{DS} = V_{DSS}$, $V_{GS} = 0V$ $T_J = 125^\circ\text{C}$			50 μA 3 mA
$R_{DS(on)}$	$V_{GS} = 10V$, $I_D = 0.5 \cdot I_{D25}$, Note 1			40 m Ω

Symbol	Test Conditions ($T_J = 25^\circ\text{C}$, Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max
g_{fs}	$V_{DS} = 10\text{V}$, $I_D = 0.5 \cdot I_{D25}$, Note 1	36	50	S
R_{Gi}	Gate Input Resistance		0.9	Ω
C_{iss}	$V_{GS} = 0\text{V}$, $V_{DS} = 25\text{V}$, $f = 1\text{MHz}$		8245	pF
C_{oss}			5510	pF
C_{rss}			18	pF
$t_{d(on)}$	Resistive Switching Times $V_{GS} = 10\text{V}$, $V_{DS} = 0.5 \cdot V_{DSS}$, $I_D = 0.5 \cdot I_{D25}$ $R_G = 2\Omega$ (External)		40	ns
t_r			42	ns
$t_{d(off)}$			60	ns
t_f			11	ns
$Q_{g(on)}$	$V_{GS} = 10\text{V}$, $V_{DS} = 0.5 \cdot V_{DSS}$, $I_D = 0.5 \cdot I_{D25}$		143	nC
Q_{gs}			40	nC
Q_{gd}			44	nC
R_{thJC}			0.14	$^\circ\text{C/W}$
R_{thCS}		0.21		$^\circ\text{C/W}$

Source-Drain Diode

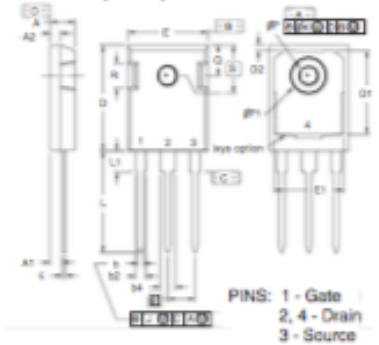
Symbol	Test Conditions ($T_J = 25^\circ\text{C}$, Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max
I_S	$V_{GS} = 0\text{V}$			80 A
I_{SM}	Repetitive, pulse Width Limited by T_{JM}			320 A
V_{SD}	$I_F = I_S$, $V_{GS} = 0\text{V}$, Note 1			1.4 V
t_{rr}	$I_F = 40\text{A}$, $-di/dt = 100\text{A}/\mu\text{s}$ $V_R = 100\text{V}$		185	ns
Q_{RM}			1.6	μC
I_{RM}			17	A

Note 1. Pulse test, $t \leq 300\mu\text{s}$, duty cycle, $d \leq 2\%$.

ADVANCE TECHNICAL INFORMATION

The product presented herein is under development. The Technical Specifications offered are derived from a subjective evaluation of the design, based upon prior knowledge and experience, and constitute a "considered reflection" of the anticipated result. IXYS reserves the right to change limits, test conditions, and dimensions without notice.

TO-247 (IXFH) Outline



SYM	INCHES		MILLIMETERS	
	MIN.	MAX.	MIN.	MAX.
A	1.96	2.00	49.8	50.8
A1	0.25	0.30	6.35	7.62
A2	0.25	0.28	6.35	7.11
B	0.45	0.50	11.4	12.7
B2	0.25	0.27	6.35	6.86
D4	0.15	0.16	3.81	4.06
C	0.24	0.25	6.10	6.35
D	0.15	0.16	3.81	4.06
D1	0.50	0.52	12.7	13.2
D2	0.50	0.52	12.7	13.2
E	0.40	0.42	10.16	10.67
E1	0.40	0.42	10.16	10.67
F	0.25	0.26	6.35	6.60
G	0.25	0.26	6.35	6.60
H	0.25	0.26	6.35	6.60
I	0.25	0.26	6.35	6.60
J	0.25	0.26	6.35	6.60
K	0.25	0.26	6.35	6.60
L	0.25	0.26	6.35	6.60
L1	0.25	0.26	6.35	6.60
M	0.25	0.26	6.35	6.60
M1	0.25	0.26	6.35	6.60
N	0.25	0.26	6.35	6.60
O	0.25	0.26	6.35	6.60
P	0.25	0.26	6.35	6.60
Q	0.25	0.26	6.35	6.60
R	0.25	0.26	6.35	6.60
S	0.25	0.26	6.35	6.60

IXYS Reserves the Right to Change Limits, Test Conditions, and Dimensions.

IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents: 4,835,592; 4,931,844; 5,049,961; 5,237,481; 6,162,665; 6,404,065B1; 6,683,344; 6,727,585; 7,005,734B2; 7,157,336B2; 4,860,072; 5,017,508; 5,063,307; 5,381,025; 6,259,123B1; 6,534,343; 6,710,405B2; 6,759,692; 7,063,975B2; 4,881,106; 5,034,796; 5,187,117; 5,486,715; 6,306,728B1; 6,583,505; 6,710,463; 6,771,478B2; 7,071,537