

## Insulated Gate Bi-Polar Transistor Type T0800EB45G

### Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
$V_{CES}$	Collector – emitter voltage	4500	V
$V_{DC\ link}$	Permanent DC voltage for 100 FIT failure rate.	2800	V
$V_{GES}$	Peak gate – emitter voltage	$\pm 20$	V

	RATINGS	MAXIMUM LIMITS	UNITS
$I_{C(DC)}$	DC collector current, IGBT	800	A
$I_{CRM}$	Repetitive peak collector current, $t_p=1ms$ , IGBT	1600	A
$I_{F(DC)}$	Continuous DC forward current, Diode	800	A
$I_{FRM}$	Repetitive peak forward current, $t_p=1ms$ , Diode	1600	A
$I_{FSM}$	Peak non-repetitive surge $t_p=10ms$ , $V_{RM}=60\%V_{RRM}$ , Diode (Note 4)	5720	A
$I_{FSM2}$	Peak non-repetitive surge $t_p=10ms$ , $V_{RM}\leq 10V$ , Diode (Note 4)	6300	A
$P_{MAX}$	Maximum power dissipation, IGBT (Note 2)	6.4	kW
$(di/dt)_{cr}$	Critical diode di/dt (note 3)	2000	A/ $\mu s$
$T_j$	Operating temperature range.	-40 to +125	$^{\circ}C$
$T_{stg}$	Storage temperature range.	-40 to +125	$^{\circ}C$

Notes: -

- 1) Unless otherwise indicated  $T_j = 125^{\circ}C$ .
- 2)  $T_{sink} = 25^{\circ}C$ , double side cooled.
- 3) Maximum commutation loop inductance 200nH.
- 4) Half-sinewave,  $125^{\circ}C$   $T_j$  initial.

## Characteristics

### IGBT Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
$V_{CE(sat)}$	Collector – emitter saturation voltage	-	2.75	3.2	$I_C = 800A, V_{GE} = 15V, T_j = 25^\circ C$	V
		-	3.50	3.9	$I_C = 800A, V_{GE} = 15V$	V
$V_{T0}$	Threshold voltage	-	-	1.74	Current range: 267 - 800A	V
$r_T$	Slope resistance	-	-	2.70		m $\Omega$
$V_{GE(TH)}$	Gate threshold voltage	-	5.2	-	$V_{CE} = V_{GE}, I_C = 85mA$	V
$I_{CES}$	Collector – emitter cut-off current	-	20	50	$V_{CE} = V_{CES}, V_{GE} = 0V$	mA
$I_{GES}$	Gate leakage current	-	-	$\pm 15$	$V_{GE} = \pm 20V$	$\mu A$
$C_{ies}$	Input capacitance	-	135	-	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$	nF
$t_{d(on)}$	Turn-on delay time	-	1.8	-	$I_C = 800A, V_{CE} = 2800V, di/dt = 1700A/\mu s$	$\mu s$
$t_r(V)$	Rise time	-	3.3	-		$\mu s$
$Q_{g(on)}$	Turn-on gate charge	-	5	-	$V_{GE} = \pm 15V, L_s = 200nH$	$\mu C$
$E_{on}$	Turn-on energy	-	6.3	-	$R_{g(ON)} = 5.6\Omega, R_{g(OFF)} = 5.6\Omega, C_{GE} = 90nF$	J
$t_{d(off)}$	Turn-off delay time	-	3.0	-	Integral diode used as freewheel diode (Note 3 & 4)	$\mu s$
$t_f(I)$	Fall time	-	2.4	-		$\mu s$
$Q_{g(off)}$	Turn-off gate charge	-	8	-		$\mu C$
$E_{off}$	Turn-off energy	-	3.7	-		J
$I_{SC}$	Short circuit current	-	2800	-	$V_{GE} = +15V, V_{CC} = 2800V, V_{CEmax} \leq V_{CES}, t_p \leq 10\mu s$	A

### Diode Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
$V_F$	Forward voltage	-	3.35	3.6	$I_F = 800A, T_j = 25^\circ C$	V
		-	3.5	3.8	$I_F = 800A$	V
$V_{T0}$	Threshold voltage	-	-	2.05	Current range 267 - 800A	V
$r_T$	Slope resistance	-	-	2.19		m $\Omega$
$I_{rm}$	Peak reverse recovery current	-	800	-	$I_F = 800A, V_{GE} = -15V, di/dt = 1700A/\mu s$	A
$Q_{rr}$	Recovered charge	-	1020	-		$\mu C$
$t_{rr}$	Reverse recovery time, 50% chord	-	1.5	-		$\mu s$
$E_r$	Reverse recovery energy	-	1.2	-		J

### Thermal Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
$R_{thJK}$	Thermal resistance junction to sink, IGBT	-	-	15.6	Double side cooled	K/kW
		-	-	25.4	Collector side cooled	K/kW
		-	-	40.5	Emitter side cooled	K/kW
$R_{thJK}$	Thermal resistance junction to sink, Diode	-	-	24.7	Double side cooled	K/kW
		-	-	37.9	Cathode side cooled	K/kW
		-	-	70.8	Anode side cooled	K/kW
F	Mounting force	25	-	35	Note 2	kN
$W_t$	Weight	-	1.2	-		kg

Notes:-

- 1) Unless otherwise indicated  $T_j = 125^\circ C$ .
- 2) Consult application note 2008AN01 for detailed mounting requirements
- 3)  $C_{GE}$  is additional gate – emitter capacitance added to output of gate drive
- 4) Figures 6 to 9 are obtained using integral diode as freewheeling diode

**Curves**

Figure 1 – Typical collector-emitter saturation voltage characteristics

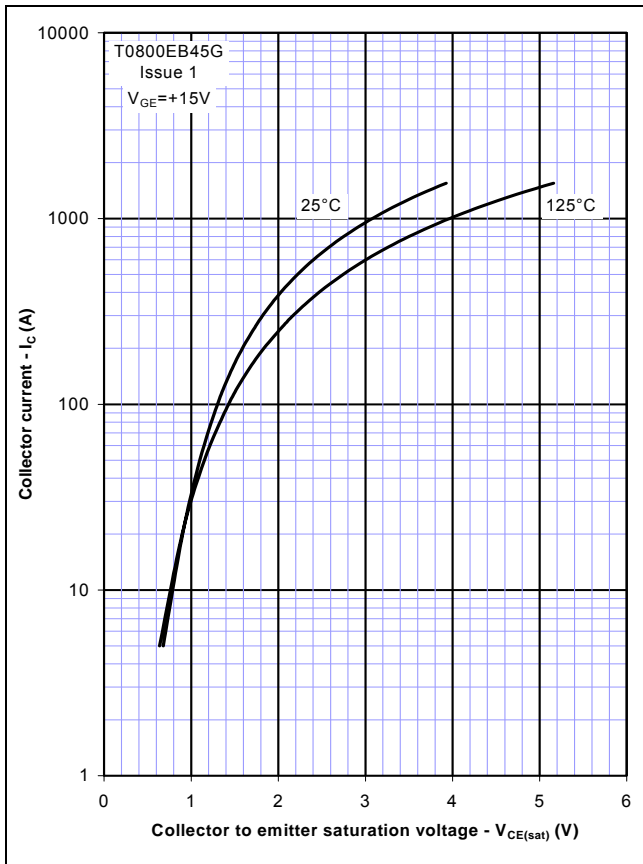


Figure 2 – Typical output characteristic

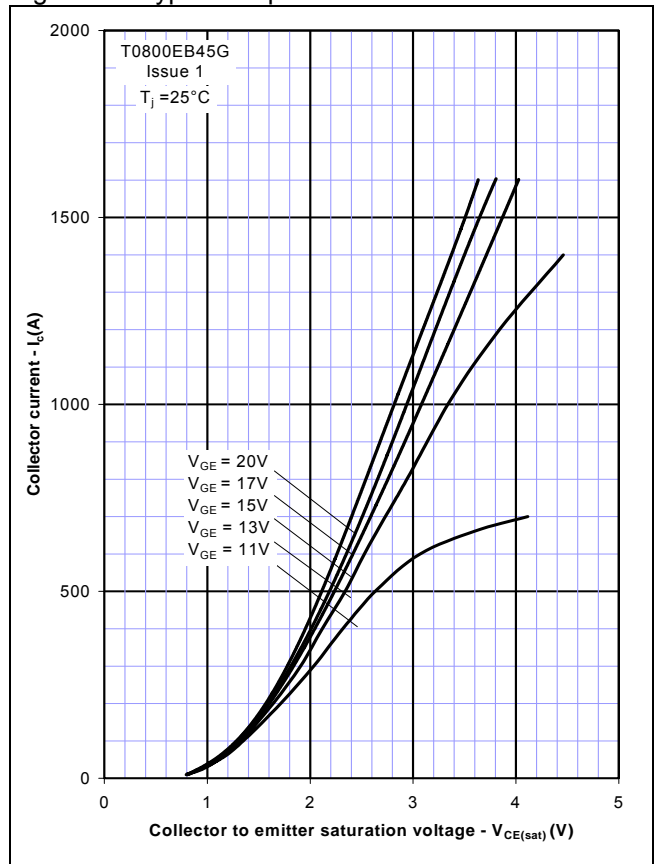


Figure 3 – Typical output characteristic

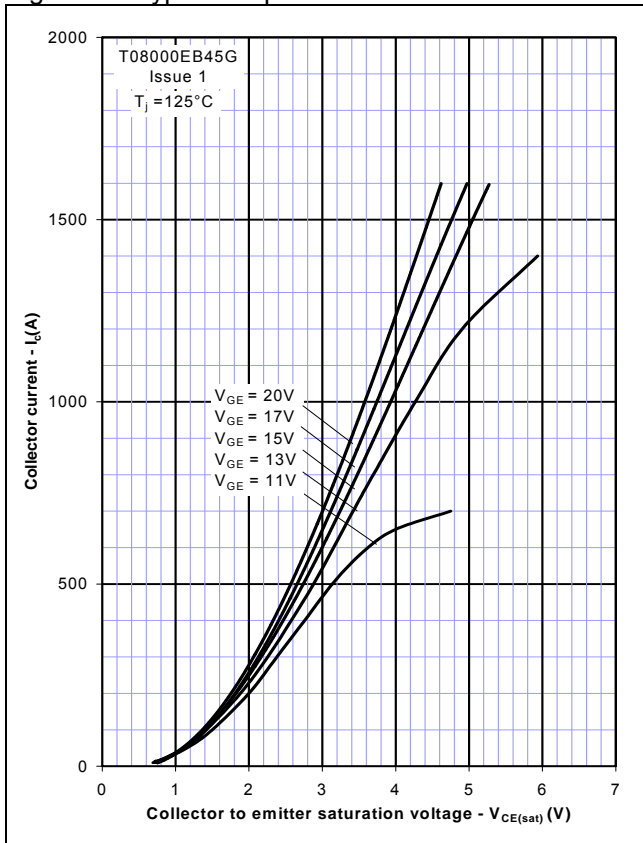


Figure 4 – Typical turn-on delay time vs gate resistance

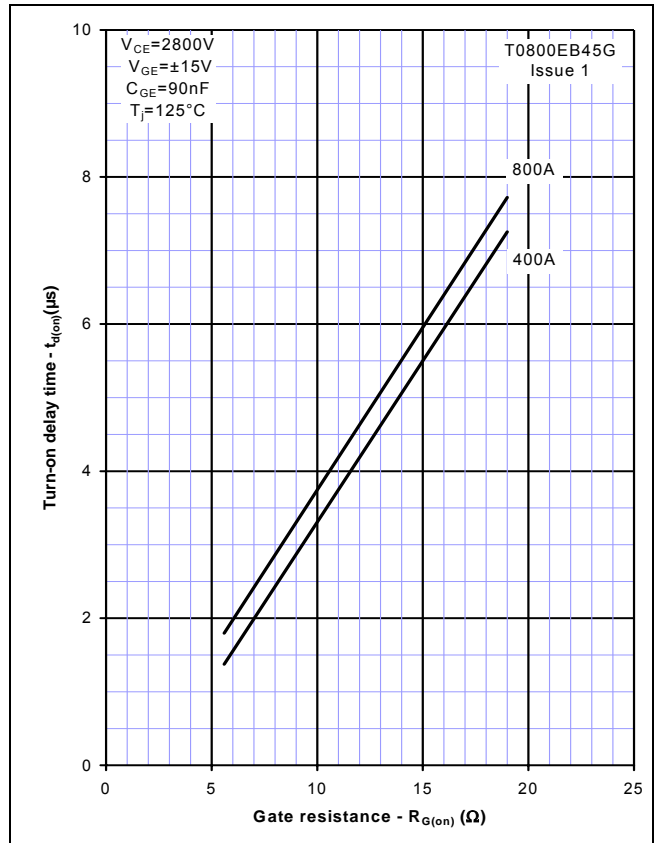


Figure 5 – Typical turn-off delay time vs. gate resistance

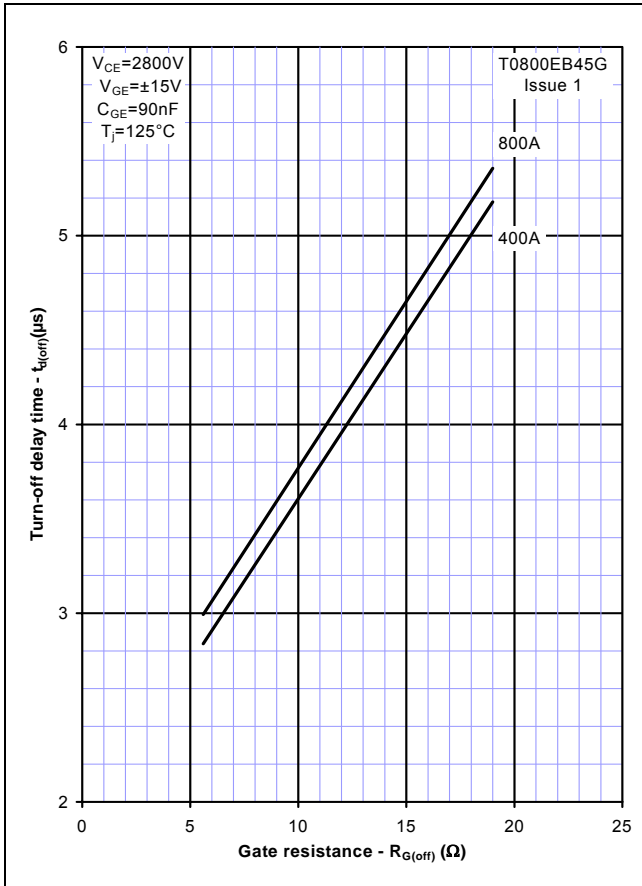


Figure 6 – Typical turn-on energy vs. collector current

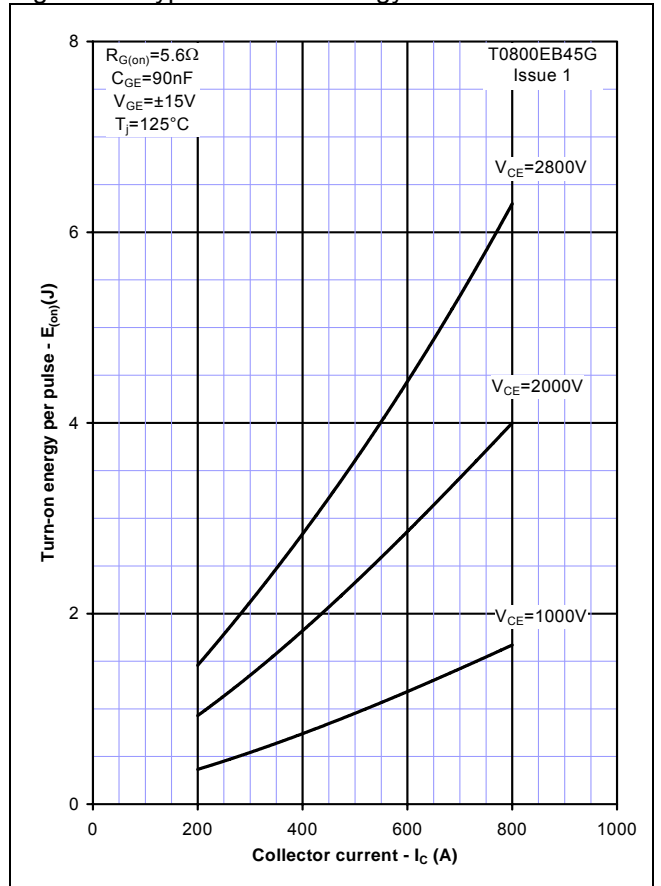


Figure 7 – Typical turn-on energy vs. di/dt

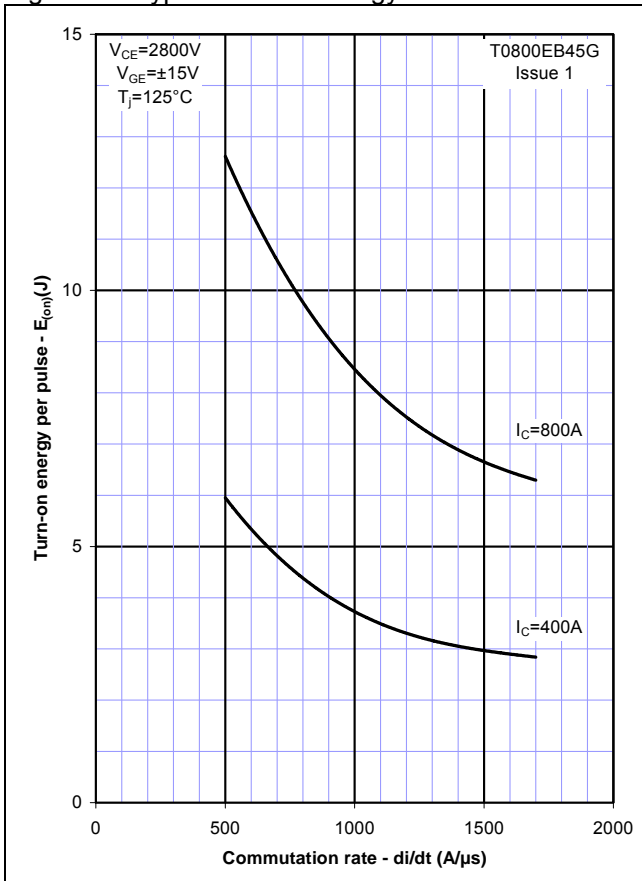


Figure 8 – Typical turn-off energy vs. collector current

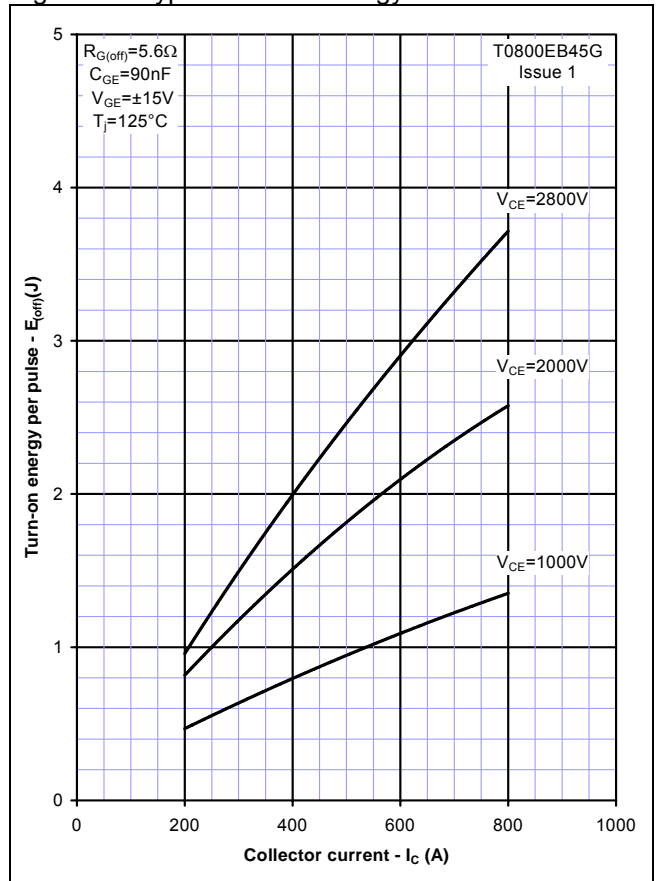


Figure 9 – Turn-off energy vs voltage

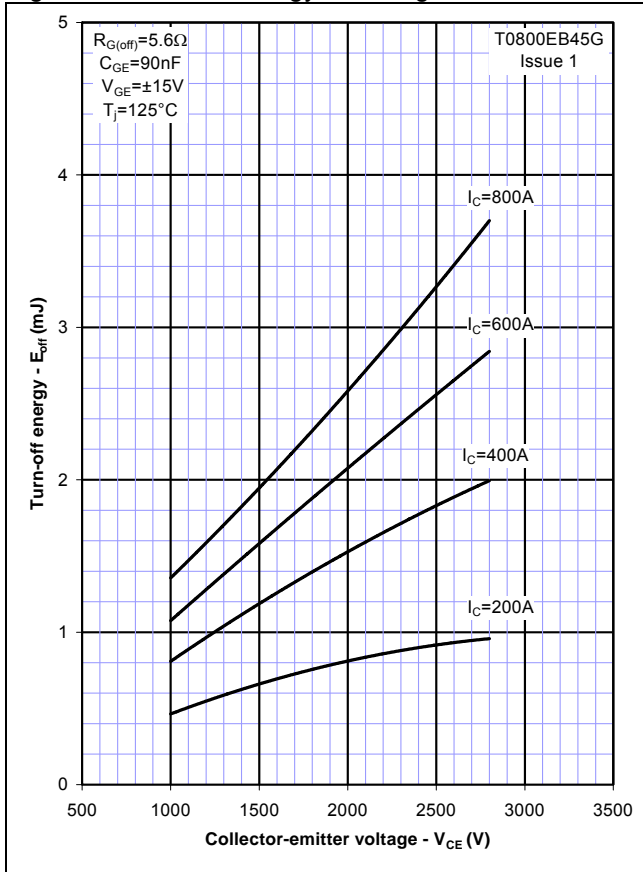


Figure 10 – Safe operating area

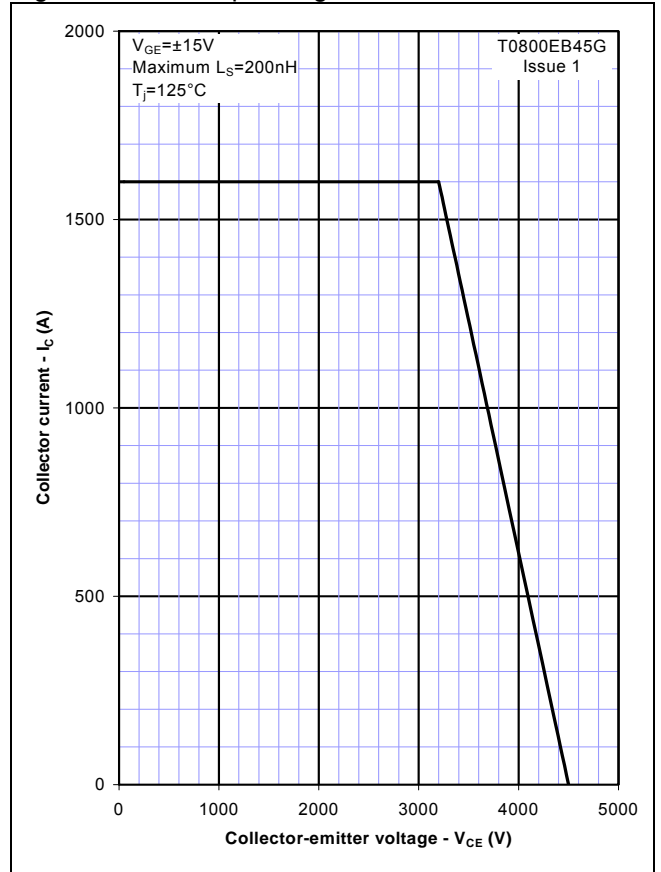


Figure 11 – Typical diode forward characteristics

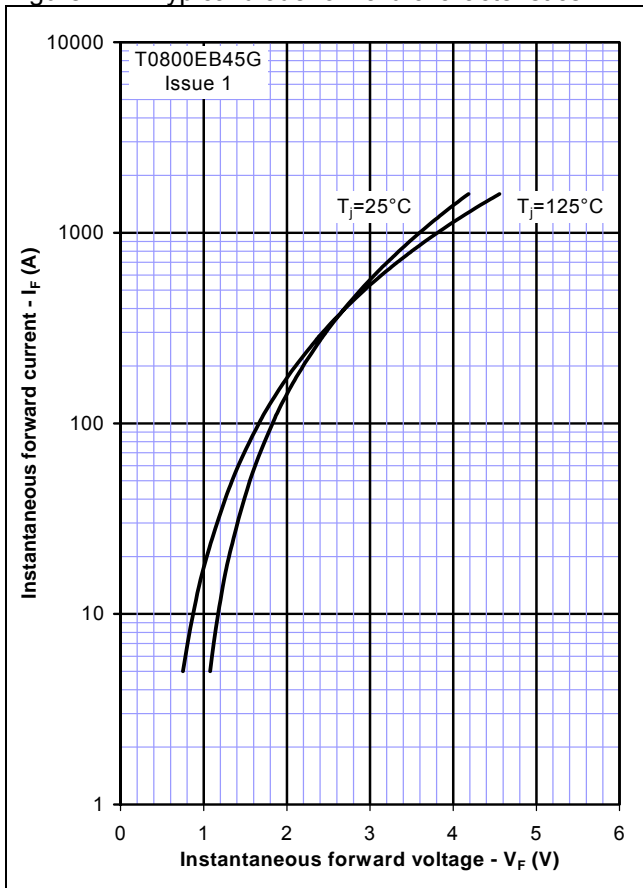


Figure 12 – Typical recovered charge

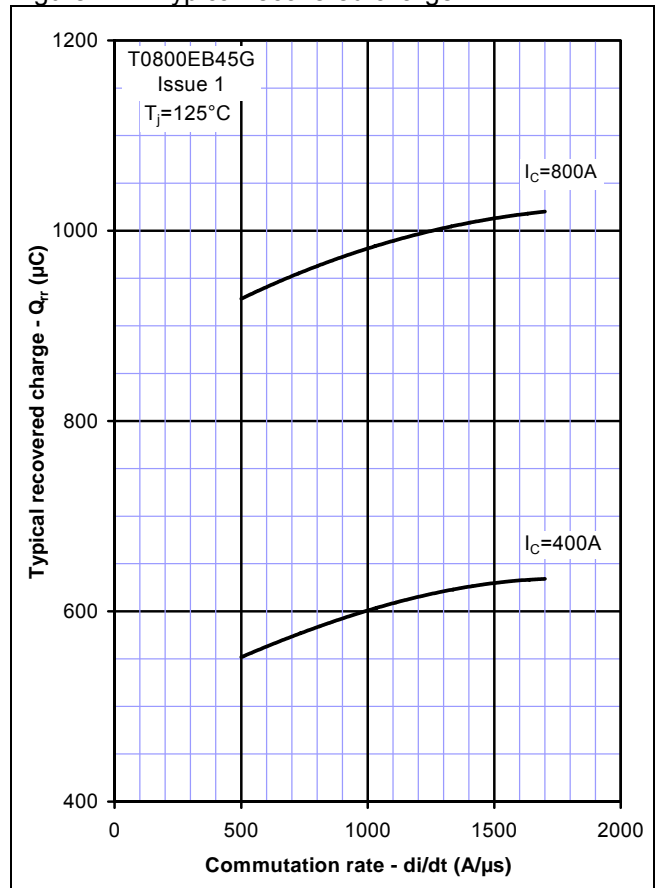


Figure 13 – Typical reverse recovery current

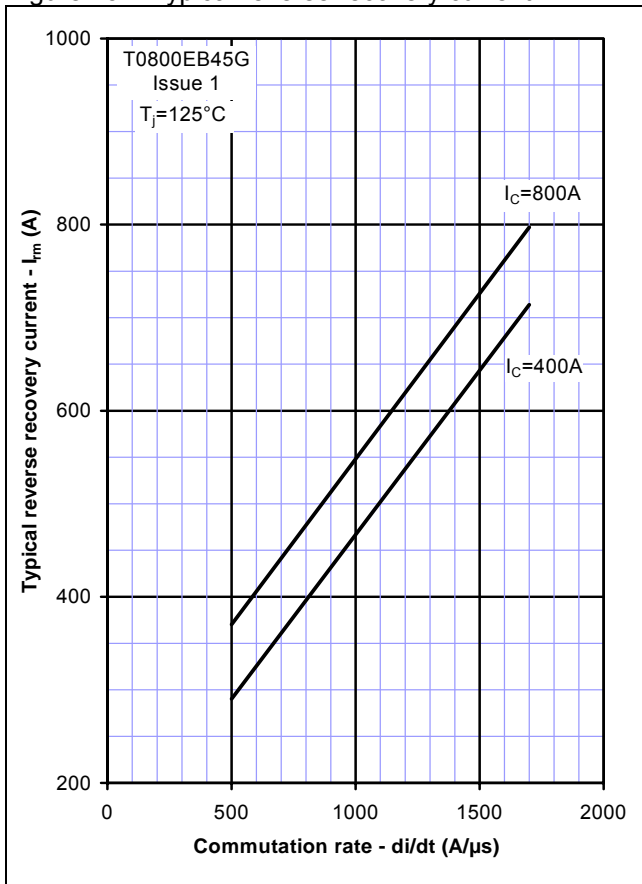


Figure 14 – Typical reverse recovery time

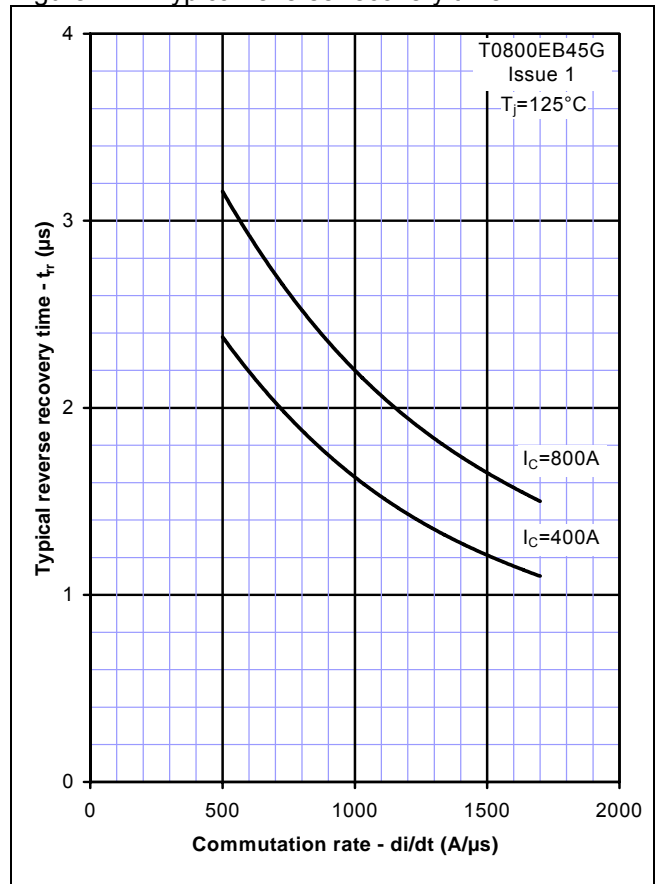


Figure 15 – Transient thermal impedance (IGBT)

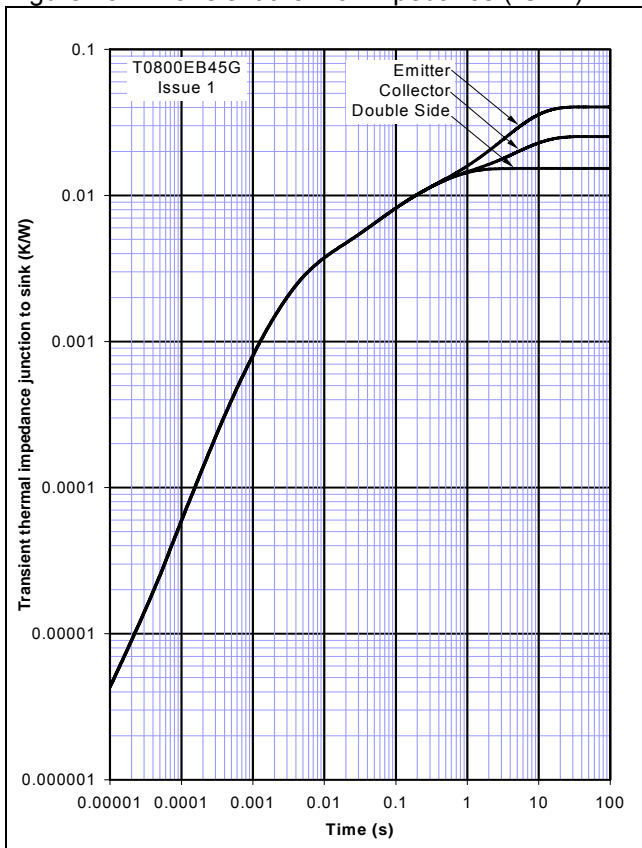
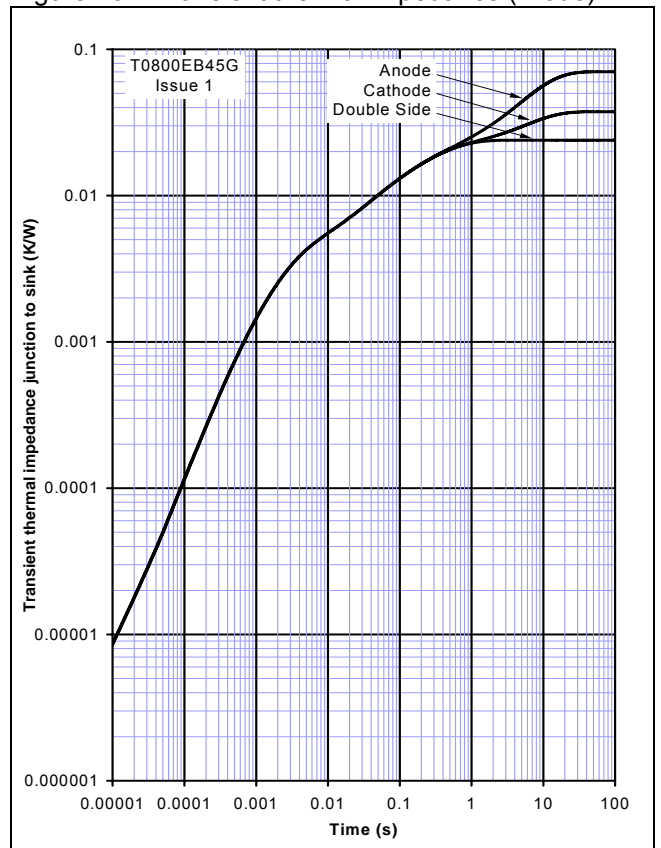
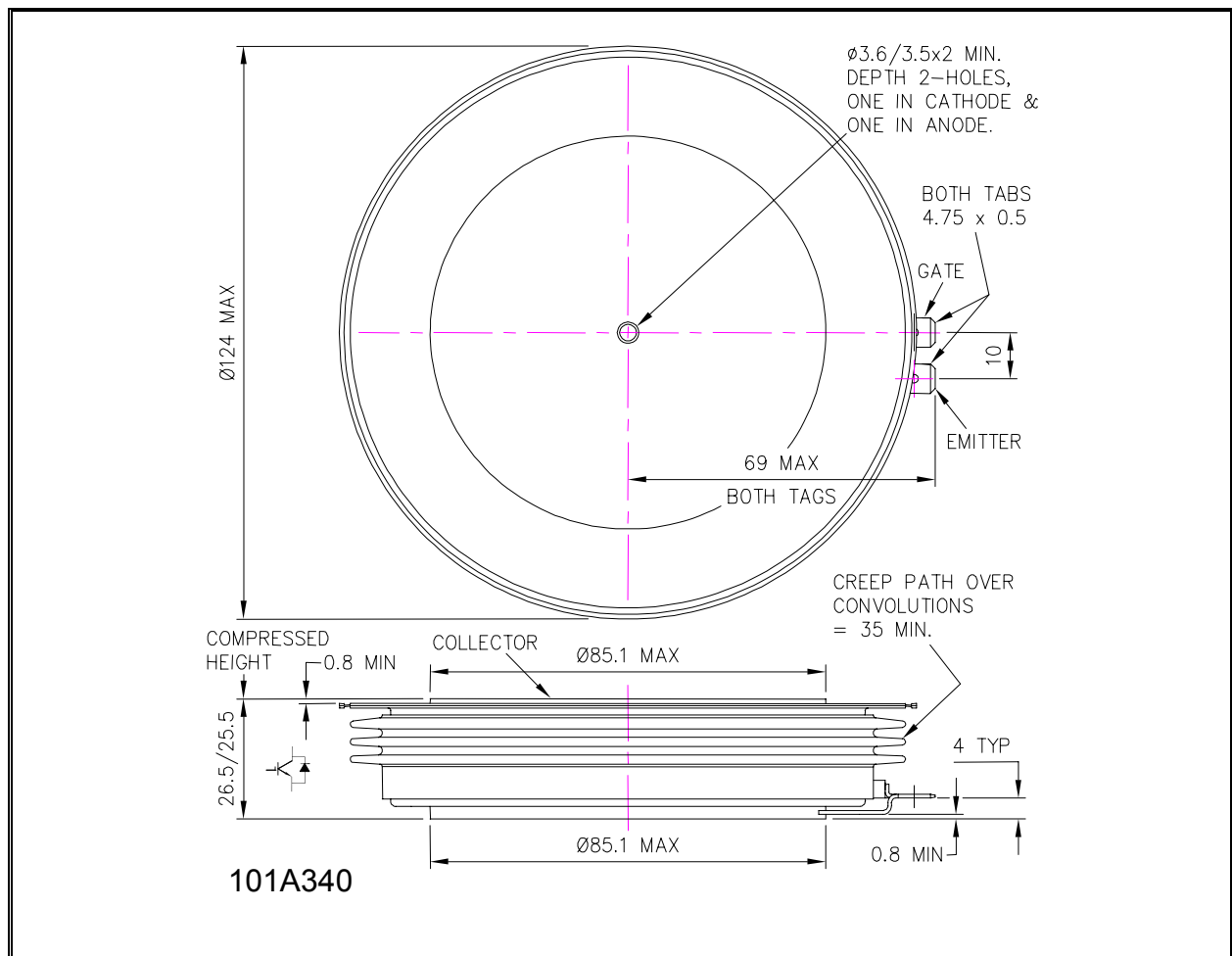


Figure 16 – Transient thermal impedance (Diode)



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<b>T0800</b>	<b>EB</b>	<b>45</b>	<b>G</b>
Fixed type Code	Fixed Outline Code	Voltage Grade $V_{CES}/100$ 45	Fixed format code

Typical order code: T0800EB45G ( $V_{CES} = 4500V$ )

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